

EAST [Untitled1:1]

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S1: (0) bidirectionS3 adj2 port42
S2: (1367) bidirectionS3 adj2 portS2
S3: (52) S2 same synchronS6
S4: (75) hysteresis same driver same receiver
S5: (3) pulhup same pulldown same transistor same signal same ready
S6: (3) pulhup same pulldown same transistor same ready
S7: (18) pulhup same pulldown same ready
S8: (19655) synchronS6 adj2 circuitS3
S9: (85) bidirectionS3 same S8
S10: (8550) ready adj3 (communicatS3 or transferS4)
S11: (480) assertS3 same S10
S12: (283) "710"/S.ccls. and S11
S13: (498) S10 same directionS3
S14: (68) S10 same bidirectionS3
S15: (53) (pull adj up) same (pull adj down) same ready
S16: (327) ((pull adj up) same (pull adj down)) and "710"/S.ccls.
S17: (73) ((pull adj up) same (pull adj down)) and "710"/305-317.ccls.
S18: (96) ((pull adj up) same (pull adj down) same transistorS2) and "710"/S.ccls.
S19: (1) ((pull adj up) same (pull adj down) same transistorS2 same bidirectionS3) and "710"/S.ccls.
S20: (27) ((pull adj up) same (pull adj down) same transistorS2 same bidirectionS3)
S21: (0) deassertS3 same assertS3 same ready
S22: (0) deassertS3 same assertS3
S23: (0) de-assertS3 same assertS3
S24: (480) de-assertS3 same assertS3 same ready
S26: (1) bidirectionS3 same S24
S25: (108) bidirectionS3 and S24
S27: (256) (710/61).CCLS.
S28: (3859) bidirectionS2 same simultaneousS3
S29: (196) "710"/S.ccls. and S28
S30: (48) bidirectionS2 same simultaneousS3 same ready
S31: (441) bidirectionS2 same simultaneousS3 same portS2
S32: (44) "710"/S.ccls. and S31
S33: (5673) bidirectionS2 adj2 bus
S34: (86) ready same S33 same signal
S35: (246) S33 same synchronS6
S36: (21) ready same S35
S37: (5828) (pull adj down) same (pull adj up) same transistorS2

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Ready

